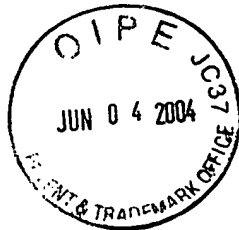


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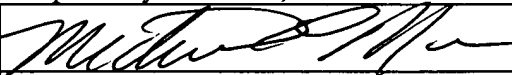
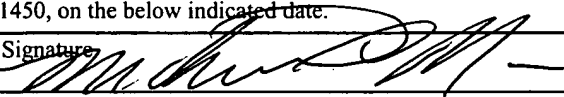
Attorney Docket No.	Serial No.
136P26US01	10/790,336

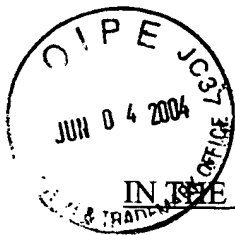
MS: Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In re Application of:	Gilbert P. Brunette et al		
Serial No.:	10/790,336	Examiner:	
Confirmation No.:		Art Unit:	
Filed:	March 1, 2004		
For:	A NOVEL RADIO FREQUENCY (RF) CIRCUIT BOARD TOPOLOGY		
We are transmitting the following documents: Return Postcard Transmittal Letter [1 page] Information Disclosure Statement (37 CFR 1.97(b)) [2 pages] Information Disclosure Statement by Applicant [2 pages] Copies of seven (7) prior art articles			

Please charge Deposit Account 50-0549 for any fees under 37 CFR §1.16 and §1.17 that may be required during the pendency of this application. This authorization includes the fee for any extension of time under 37 CFR §1.136(a) that may be necessary. To the extent any such extension should become necessary it is hereby requested.

Respectfully submitted,

Registration No. 30,087	Direct Dial 612-331-7415	
Date: June 1, 2004	Michael L. Mau	
United States Patent and Trademark Office Customer No. 23322		
Certificate of Mailing		
Pursuant to 37 CFR 1.8, I certify that this correspondence is being deposited in the United States mail, addressed to: MS: Amendments, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below indicated date.		
		Signature 
Date of Mailing June 1, 2004	Printed Name Michael L. Mau	



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Gilbert P. Brunette et al Examiner:
Serial No.: 10/790,336 Art Unit:
Filed: March 1, 2004 Confirmation No.:
Docket No.: 136P26US01
For: A NOVEL RADIO FREQUENCY (RF) CIRCUIT BOARD
TOPOLOGY

INFORMATION DISCLOSURE STATEMENT (37 C.F.R. §1.97(b))

MS: Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted within three months of the filing date of the above-identified application. Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with the Notice signed July 11, 2003 at 1276 OG 55, copies of the U.S. patents listed on the enclosed Form 1449 are not enclosed. In accordance with 37 C.F.R. §1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked

as being considered and initialled by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 500549.

Respectfully submitted,

GILBERT P. BRUNETTE ET AL

Date: 6-1-04

By: 

Michael L. Mau
Reg. No. 30,087
IPLM Group, P.A.
Post Office Box 18455
Minneapolis, MN 55418
Telephone (612) 331-7415

MLM:das

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Substitute for form 1449/PTO

JUN 04 2004

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/790,336
Filing Date	03/01/2004
First Named Inventor	Brunette et al
Art Unit	
Examiner Name	
Attorney Docket Number	136P26US01

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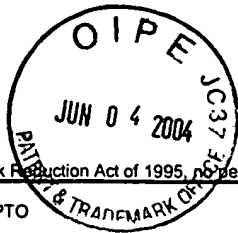
Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not it is included in the list of references.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/790,336
Filing Date	03-01-2004
First Named Inventor	Brunette et al
Art Unit	
Examiner Name	
Attorney Docket Number	136P26US01

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SARFARAZ et al; Interconnect Characterization...Digital Applications; (rambus.com); IEEE 9th Topical Meeting Electrical Performance; Oct23-25, 2000pp1-4; Mountain View, CA, US	
		CHEN et al; Via and Return Path Discontinuity Impact on High Speed Digital Signal Quality; EPEP Conf. www.sigrity.com; Oct 2000; pp1-4	
		ULTRACAD DESIGN INC.; The Effects of Vias on PCB Traces; ultracad.com; 994; pp1-6; Bellevue, WA, US	
		COHEN, TOM; Practical Guidelines for the implementation of Back Drilling Plated Through Hole Vias in Multi-gigabit Board Applications; DesignCon 2003; pp1-10; Nashua, NH, US	
		ZEGARAC, Zoran; Signal Integrity issue of dense pin-field and via interconnections; pp1-4; University of Western Australia	
		COHEN et al; Signal Integrity Considerations for High Speed Data Transmission in a Printed Circuit Board System; IMAPS 1998; www.teradyne.com;pp1-6	
		TALLO, Joseph; Designing an X-Band T/R Module Package; Kyocera; Ansoft 2003 HFSS Users Workshop; www.ansoft.com; pp1-32	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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